

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: K. KANETANI, et al.

Application No.: Rule 1.53(b) Continuation of U.S. Patent Application
Serial No. 10/230,295, filed August 29, 2002

Filed: On Even Date Herewith

For: SEMICONDUCTOR INTEGRATED CIRCUIT AND
SEMICONDUCTOR LOGIC CIRCUIT USED IN THE
INTEGRATED CIRCUIT

Art Group of Parent: 2819

Examiner of Parent: A. Q. Tran

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.97 & 1.98

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

January 12, 2004

Sir:

In the matter of the above-identified application, this Information Disclosure Statement is being submitted with the following citation as specified in 37 CFR §1.97(d).

"A copy of any patent, publication or other information listed in an Information Disclosure Statement is not required to be provided if it was previously cited by or submitted to the Office in a prior application, provided that the prior application is properly identified in the statement and relied upon for an earlier filing date under 35 U.S.C. §120."

Applicants are submitting herewith a copy of Form PTO/SB/08A which list documents cited in parent applications Serial No. 10/230,295, filed August 29, 2002 which is a divisional of application Serial No. 09/840,190, filed April 24, 2001; which


is a divisional of Serial No. 09/437,268, filed November 10, 1999 (now U.S. Patent No. 6,369,617), the entire disclosures of which are hereby incorporated by reference.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

To the extent necessary, the applicants petition for an extension of time under 37 CFR 1.136. Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus, LLP Deposit Account No. 01-2135 (520.37857CV3).

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

By 
Gregory E. Montone
Reg. No. 28,141

GEM/dlt

1300 North Seventeenth Street, Suite 1800
Arlington, Virginia 22209
Telephone: (703) 312-6600
Facsimile: (703) 312-6666

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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>			Complete if Known	
			Application Number	Not assigned yet
			Filing Date	On even date herewith
			First Named Inventor	K. KANETANI, et al.
			Art Unit	Not assigned yet
Examiner Name	Not assigned yet			
Attorney Docket Number	520.37857CV3			
Sheet	1	of	1	

U.S. PATENT DOCUMENTS						
Examiner Initials ¹	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)				
		5.291.076		03-1994	Bridges et al.	
		5.640.108		06-1997	Miller	
		5.373.203		12-1994	Nicholes et al.	
		6.316.961		11-2001	Kanetani et al.	

FOREIGN PATENT DOCUMENTS							
Examiner Initials ¹	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)					
		JP 10-150358		06-1998			

OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS			
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¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard St.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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